Mindluster Platform

About the course C Based VLSI Design Techniques

C-Based VLSI Design course, in this course Explore methodologies and tools for efficient digital and analog circuit design using the C programming language.

Programming Category's Courses

Course Lesson(44)

Lesson 1 : **C Based VLSI Design Intro Video**

Lesson 2 : **Lec 1 Introduction to C Based VLSI Design**

Lesson 3 : **Lec 2 C based VLSI Design An Overview**

Lesson 4 : **Lec 3 C based VLSI Design Problem Formulation**

Lesson 5 : **Lec 4 C based VLSI Design Course Plan**

Lesson 6 : **Lec 5 Introduction to Scheduling**

Lesson 7 : **Lec 6 ILP formulation of Scheduling**

Lesson 8 : **Lec 7 ILP formulation of MRLC and MLRC Scheduling**

Lesson 9 : **Lec 8 Multiprocessor Scheduling**

Lesson 10 : **Lec 9 Hu s algorithm for Multiprocessor Scheduling**

Lesson 11 : **Lec 10 List based Scheduling of MLRC**

Lesson 12 : **Lec 11 List based Scheduling of MRLC**

Lesson 13 : **Lec 12 Forced Directed Scheduling**

Lesson 14 : **Lec 13 Forced Directed MLRC and MRLC Scheduling Algorithm**

Lesson 15 : **Lec 14 Path Based Scheduling**

Lesson 16 : **Lec 15 Path Based Scheduling**

Lesson 17 : **Lec 16 Allocation and Binding Problem Formulation**

Lesson 18 : **Lec 17 Left Edge Algorithm**

Lesson 19 : **Lec 18 ILP Formulation of Allocation and Binding**

Lesson 20 : **Lec 19 Allocation and Binding for Hierarchical Graph**

Lesson 21 : **Lec 20 Register Allocation and Binding**

Lesson 22 : **Lec 21 Multi port Binding Problem**

Lesson 23 : **Lec 22 Datapath and Controller Synthesis**

Lesson 24 : **Lec 23 HLS for Arrays**

Lesson 25 : **Lec 24 HLS for Loops**

Lesson 26 : **Lec 25 HLS for Loop pipeline**

Lesson 27 : **Lec 26 Hardware Efficient C Coding**

Lesson 28 : **Lec 27 Hardware Efficient C Coding â€“ part II**

Lesson 29 : **Lec 28 Dataflow Optimization in HLS**

Lesson 30 : **Lec 29 Frontend Optimizations in C**

Lesson 31 : **Lec 30 HLS Optimizations Case Study 1**

Lesson 32 : **Lec 31 HLS Optimizations Case Study 1**

Lesson 33 : **Lec 32 Simulation based Verification**

Lesson 34 : **Lec 33 RTL to C Reverse Engineering**

Lesson 35 : **Lec 34 Phase wise Verification of HLS**

Lesson 36 : **Lec 35 Equivalence between C and RTL**

Lesson 37 : **Lec 36 Introduction to Hardware Security**

Lesson 38 : **Lec 37 HLS for Security**

Lesson 39 : **Lec 38 Attacks on RTL Logic locking**

Lesson 40 : **Lec 39 Introduction to Logic Synthesis**

Lesson 41 : **Lec 40 FPGA Technology Mapping**

Lesson 42 : **Lec 41 Introduction to Physical Synthesis**

Lesson 43 : **Lec 42 Introduction to Circuit optimizations**

Lesson 44 : **Lec 43 Recent Advances in C Based VLSI Design**

Related courses

React Projects Workshops

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