Mindluster Platform

About the course Computer Architecture Topics

Computer Architecture Topics course in hindi, This course covers advanced concepts and emerging trends in computer architecture, exploring cutting-edge developments in the field.

Computer Science Category's Courses

Course Lesson(34)

Lesson 1 : **Advanced Computer Architecture Intro Video**

Lesson 2 : **Lec 1 Review of Basic Computer Organization**

Lesson 3 : **Lec 2 Perfomance Evaluation Methods**

Lesson 4 : **Lec 3 Introduction to RISC Instruction Pipeline**

Lesson 5 : **Tutorial 1 Instruction Pipeline and Performance**

Lesson 6 : **Lec 4 Pipeline Hazards**

Lesson 7 : **Lec 5 Control Hazards Branch Prediction**

Lesson 8 : **Lec 6 MIPS Pipeline for Multi Cycle Operations**

Lesson 9 : **Tutorial 2 Pipeline Hazard Analysis**

Lesson 10 : **Lec 7 Compiler Techniques to Explore ILP**

Lesson 11 : **Lec 8 Dynamic Scheduling to Explore ILP**

Lesson 12 : **Lec 9 Dynamic Scheduling with Tomasulo s Algorithm**

Lesson 13 : **Lec 10 Dynamic Scheduling with Speculative Execution**

Lesson 14 : **Tutorial 3 Static and Dynamic Scheduling**

Lesson 15 : **Lec 11 Advanced Pipelining Superscalar Processors**

Lesson 16 : **Lec 12 Exploiting DLP Vector GPU Architectures**

Lesson 17 : **Tutorial 4 Architectural Simulation using gem5**

Lesson 18 : **Tutorial 5 Core Optimization in gem5**

Lesson 19 : **Lec 13 Introduction to Cache Memory**

Lesson 20 : **Lec 14 Block Replacement Techniques Write Strategy**

Lesson 21 : **Tutorial 6 Design Concepts in Cache Memory**

Lesson 22 : **Lec 15 Optimization Techniques in Cache Memory**

Lesson 23 : **Lec 16 Advanced Cache Optimization Techniques**

Lesson 24 : **Tutorial 7 Optimization Techniques in Cache Memory**

Lesson 25 : **Tutorial 8 Cache Optimization in gem5**

Lesson 26 : **Lec 17 Introduction to DRAM System**

Lesson 27 : **Lec 18 DRAM Controllers Address Mapping**

Lesson 28 : **Lec 19 Secondary Storage Systems**

Lesson 29 : **Tutorial 9 Design Concepts in DRAM and Harddisk**

Lesson 30 : **Lec 20 Tiled Chip Multicore Processors**

Lesson 31 : **Lec 21 Routing Techniques in Network on Chip**

Lesson 32 : **Lec 22 NoC Router Microarchitecture**

Lesson 33 : **Lec 23 How to Explore Computer Architecture**

Lesson 34 : **Tutorial 10 TCMP and NoC Design Principles**

Related courses

Linear Algebra for Computer Scientists

Insertion Sort

Bubble Sort

GCSE Computer Science

Random Access Memory

Binary Trees

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