Mindluster Platform

About the course Random Access Memory

What is random access memory? RAM (Random Access Memory) is the internal memory of the CPU for storing data, program, and program result. It is a read/write memory which stores data until the machine is working. As soon as the machine is switched off, data is erased. ... Data in the RAM can be accessed randomly but it is very expensive.What is RAM and its function? Computer memory or random access memory (RAM) is your system's short-term data storage; it stores the information your computer is actively using so that it can be accessed quickly. The more programs your system is running, the more memory you'll need.What is difference between RAM & ROM? RAM, which stands for random access memory, and ROM, which stands for read-only memory, are both present in your computer. RAM is volatile memory that temporarily stores the files you are working on. ROM is non-volatile memory that permanently stores instructions for your computer. Find out more about RAM.What is stored in RAM? RAM stands for Random Access Memory. Physically, it is a series of chips in your computer. When your computer is turned on, it loads data into RAM. Programs that are currently running, and open files, are stored in RAM; anything you are using is running in RAM somewhere.Is Random Access Memory permanent or temporary? RAM, which stands for random access memory, and ROM, which stands for read-only memory, are both present in your computer. RAM is volatile memory that temporarily stores the files you are working on. ROM is non-volatile memory that permanently stores instructions for your computer. Find out more about RAM.

Computer Science Category's Courses

Course Lesson(7)

Lesson 1 : **Dynamic Random Access Memory DRAM Part 1 Memory Cell Arrays**

Lesson 2 : **Dynamic Random Access Memory DRAM Part 2 Read and Write Cycles**

Lesson 3 : **Dynamic Random Access Memory DRAM Part 3 Binary Decoders**

Lesson 4 : **Dynamic Random Access Memory DRAM Part 4 Multiplexers and Demultiplexers**

Lesson 5 : **Dynamic Random Access Memory DRAM Part 5 DIMM Organisation**

Lesson 6 : **Dynamic Random Access Memory DRAM Part 6 Burst Mode and Bank Interleaving**

Lesson 7 : **Dynamic Random Access Memory DRAM Part 7 Memory Address Mapping**

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for Business Contact
business@mindluster.com