Mindluster Platform

# About the course Organization and Architecture COA

Organizational architecture or organizational space: the influence of the spatial environment on humans in and around organizations. Organizational architecture or organization design: the creation of roles, processes, and formal reporting relationships in an organization.

**Computer Softwares Category's Courses**

# Course Lesson(81)

Lesson 1 : **Binary coded decimal | BCD | COA| Lec 1| Bhanu Priya**

Lesson 2 : **unsigned integer representation | COA | Lec 2 | Bhanu Priya**

Lesson 3 : **sign magnitude representation**

Lesson 4 : **one s complement representation**

Lesson 5 : **2 s complement of binary number**

Lesson 6 : **Floating point representation | IEEE 754 | COA | Lec 6 | Bhanu Priya**

Lesson 7 : **Floating point representation | Example 1 | COA | Lec 7 | Bhanu Priya**

Lesson 8 : **Floating point representation | Example 2 | COA | Lec 8 | Bhanu Priya**

Lesson 9 : **Floating point representation | Example 3 | COA | Lec 9 | Bhanu Priya**

Lesson 10 : **Floating point representation | Example 4 | COA | Lec 10 | Bhanu Priya**

Lesson 11 : **Fixed point representation | Unsigned | COA | Lec 11 | Bhanu Priya**

Lesson 12 : **Fixed point representation | Signed | COA | Lec 12 | Bhanu Priya**

Lesson 13 : **Fixed point representation | Example 1 | COA | Lec 13 | Bhanu Priya**

Lesson 14 : **Fixed point representation | Example 2 | COA | Lec 14 | Bhanu Priya**

Lesson 15 : **Fixed point representation | Example 3 4 | COA | Lec 15 | Bhanu Priya**

Lesson 16 : **Fixed point representation | Example 5 | COA | Lec 16 | Bhanu Priya**

Lesson 17 : **micro operations introduction**

Lesson 18 : **memory transfer in computer architecture**

Lesson 19 : **bus architecture in computer organization**

Lesson 20 : **bus arbitration in computer organization**

Lesson 21 : **daisychain arbitaration | COA |**

Lesson 22 : **general register organization | COA |**

Lesson 23 : **subroutine in Computer organization | COA**

Lesson 24 : **Arithmetic logic shift unit | Hardware implementation | COA**

Lesson 25 : **Logical shift micro operation | Left Right | COA | Lec 25 | Bhanu Priya**

Lesson 26 : **Circular shift micro operation | Left Right | COA | Lec 26 | Bhanu Priya**

Lesson 27 : **Arithmetic shift operations | Left Right | COA | Lec 27 | Bhanu Priya**

Lesson 28 : **Shifting operation hardware implementation | COA**

Lesson 29 : **logic micro operations | AND| NAND | OR| XOR| NOR**

Lesson 30 : **logic micro operation applications | COA**

Lesson 31 : **booths algoritham in computer organization| multiplication | COA**

Lesson 32 : **pipelining processing in computer organization |COA**

Lesson 33 : **instruction types in computer architecture**

Lesson 34 : **priority interrupts | COA**

Lesson 35 : **computer registers in computer architecture**

Lesson 36 : **computer instructions in computer architecture**

Lesson 37 : **instruction codes in computer architecture | COA**

Lesson 38 : **timing and control in computer organization | COA**

Lesson 39 : **instruction cycle in computer organization | COA**

Lesson 40 : **memory reference instructions in computer organization | COA**

Lesson 41 : **hardwired Control unit in computer organization |COA**

Lesson 42 : **microprogrammed control unit | introduction | COA**

Lesson 43 : **microprogrammed control unit | structure | COA**

Lesson 44 : **programmed i o in computer architecture | COA**

Lesson 45 : **interrupt controlled i o in computer organization | COA |**

Lesson 46 : **data transfer | parallel and serial |**

Lesson 47 : **synchronous data transfer | COA**

Lesson 48 : **asynchronus data transfer**

Lesson 49 : **asynchronous data transfer | handshaking |**

Lesson 50 : **asynchronous data transfer | strobe control |**

Lesson 51 : **DMA controller basic operation**

Lesson 52 : **design and implementation of CPU | COA**

Lesson 53 : **accumulator design in computer architecture | COA**

Lesson 54 : **I O interface | COA**

Lesson 55 : **IO interface parallel port | COA**

Lesson 56 : **IO interface serial port | COA**

Lesson 57 : **io interface with PCI |USB | SCSI | COA**

Lesson 58 : **input output processor | COA**

Lesson 59 : **inter process communication | part 1 2| IPC | COA**

Lesson 60 : **inter process communication methods | part 2 2 |COA**

Lesson 61 : **memory mapped i o in computer organization | part 1 2 | COA**

Lesson 62 : **memory mapped i o in computer organization | part 2 2 | COA**

Lesson 63 : **memory mapped CPU | RAM | ROM**

Lesson 64 : **three state bus buffer**

Lesson 65 : **stack introduction | PUSH POP |**

Lesson 66 : **stack organization | register stack memory stack |**

Lesson 67 : **memory interleaving | COA**

Lesson 68 : **SRAM and DRAM | memory cells**

Lesson 69 : **risc architecture | COA**

Lesson 70 : **cisc architecture | COA**

Lesson 71 : **risc and cisc in computer architecture**

Lesson 72 : **8085 pin diagram**

Lesson 73 : **8085 architecture**

Lesson 74 : **8085 addressing modes**

Lesson 75 : **interrupts in 8085 microprocessor**

Lesson 76 : **8085 instructions**

Lesson 77 : **8085 instruction set introduction**

Lesson 78 : **8085 instruction set with opcodes | arithmetic and data transfer |**

Lesson 79 : **instruction set of 8085 | logical bit manipulator |**

Lesson 80 : **memory organization in computer architecture**

Lesson 81 : **cache memory in computer architecture**

# Related courses

**Excel Power Query**

**Excel VBA**

**Excel Dashboard**

**Excel Course Basic to Advanced**

**Excel Functions**

**Excel Charting**

for Business Contact
business@mindluster.com