Mindluster Platform

About the course Placement VLSI in Physical Design

Placement VLSI course in hindi, in this course covers chip component arrangement, optimizing space, power, and performance in Very Large Scale Integration chip design

Engineering Category's Courses

Course Lesson(18)

Lesson 1 : **PD Lec 31 Introduction to Placement | VLSI | Physical Design**

Lesson 2 : **PD Lec 32 Placement of std cells | VLSI | Physical Design**

Lesson 3 : **PD Lec 33 Placement and Optimization | VLSI | Physical Design**

Lesson 4 : **PD Lec 34 place opt understanding | VLSI | Physical Design**

Lesson 5 : **PD Lec 35 Scan Chain Optimization | VLSI | Physical Design**

Lesson 6 : **PD Lec 36 Cell Density of std cells | VLSI | Physical Design**

Lesson 7 : **PD Lec 37 Pin Density of std cells | VLSI | Physical Design**

Lesson 8 : **PD Lec 38 Global Route Congestion | VLSI | Physical Design**

Lesson 9 : **PD Lec 39 CMOS Latch Up | VLSI | Physical Design**

Lesson 10 : **PD Lec 40 Well Tap Cell | VLSI | Physical Design**

Lesson 11 : **PD Lec 41 Tie Cell | tie low| tie high | VLSI | Physical Design**

Lesson 12 : **PD Lec 42 SVT LVT HVT Cell variants | VLSI | Physical Design**

Lesson 13 : **PD Lec 43 Timing Fixes in placement | Part 1 | VLSI | Physical Design**

Lesson 14 : **PD Lec 44 Timing Fixes in placement | Part 2 | VLSI | Physical Design**

Lesson 15 : **PD Lec 45 Spare Cells | Physical Only Cells | VLSI | Physical Design**

Lesson 16 : **PD Lec 46 Useful Skew | Timing Fixes in placement | VLSI | Physical Design**

Lesson 17 :
PD Lec 47 concurrent clock and data optimization| CCD| Timing | placement | VLSI | Physical Design

Lesson 18 : **PD Lec 48 Interview Questions | placement | VLSI | Physical Design**

Related courses

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