Mindluster Platform

About the course Static Timing Analysis Fundamentals

Static Timing Analysis course in hindi, in this course covers techniques and tools for evaluating and optimizing the timing performance of digital integrated circuits

Engineering Category's Courses

Course Lesson(41)

Lesson 1 : **introduction to static timing analysis | STA | VLSI**

Lesson 2 : **STA lec1 basics of static timing analysis | static timing analysis tutorial | VLSI**

Lesson 3 :
STA lec2 cmos basics | cmos characterestics | noise margins | static timing analysis tutorial | VLSI

Lesson 4 :
STA lec3 CMOS modelling | pin capacitance modelling | static timing analysis tutorial | VLSI

Lesson 5 :
STA lec4 CMOS switching waveform and propagation delay | static timing analysis tutorial | VLSI

Lesson 6 : **STA lec5 Clock Slew and Skew part 1 | static timing analysis tutorial | VLSI**

Lesson 7 : **STA lec6 Clock skew part 2 | static timing analysis tutorial | VLSI**

Lesson 8 : **STA lec7 clock uncertainity and unateness | static timing analysis tutorial | VLSI**

Lesson 9 : **STA lec8 setup time concepts part 1 | static timing analysis tutorial | VLSI**

Lesson 10 : **STA lec9 setup time concepts part 2 | static timing analysis tutorial | VLSI**

Lesson 11 : **STA lec10 hold time concepts | static timing analysis tutorial | VLSI**

Lesson 12 : **STA lec11 std cell propagation delay | static timing analysis tutorial | VLSI**

Lesson 13 : **STA lec 12 delay modelling in library | static timing analysis tutorial | VLSI**

Lesson 14 : **STA lec13 defining constraints | static timing analysis tutorial | VLSI**

Lesson 15 : **STA lec14 defining reg2reg constraints | static timing analysis tutorial | VLSI**

Lesson 16 :
STA lec15 defining input output constraints part 1 | static timing analysis tutorial | VLSI

Lesson 17 :
STA lec16 defining input output constraints part 2 | static timing analysis tutorial | VLSI

Lesson 18 : **sta lec17 Understanding timing report part 1 | static timing analysis tutorial | VLSI**

Lesson 19 : **sta lec18 understanding timing report part 2 | Static Timing Analysis tutorial | VLSI**

Lesson 20 : **sta lec19 understanding timing report part3 | Static Timing Analysis tutorial | VLSI**

Lesson 21 : **sta lec20 setup hold timing fixes part1 | Static Timing Analysis tutorial | VLSI**

Lesson 22 : **sta lec21 hold timing fixes in path part2 | Static Timing Analysis tutorial | VLSI**

Lesson 23 : **sta lec22 timing exceptions part 1 | false path | Static Timing Analysis tutorial | VLSI**

Lesson 24 :
sta lec23 timing exceptions part2 | multi cycle path | Static Timing Analysis tutorial | VLSI

Lesson 25 : **sta lec24 | Half Cycle Path | Static Timing Analysis tutorial | VLSI**

Lesson 26 : **sta lec25 recovery and removal checks | Static Timing Analysis tutorial | VLSI**

Lesson 27 : **sta lec26 gba pba analysis | Static Timing Analysis tutorial | VLSI**

Lesson 28 : **sta lec27 timing across clk domains part1 | Static Timing Analysis tutorial | VLSI**

Lesson 29 : **sta lec28 timing across clk domains part2 | Static Timing Analysis tutorial | VLSI**

Lesson 30 : **sta lec29 timing across clk domains part3 | Static Timing Analysis tutorial | VLSI**

Lesson 31 : **sta lec30 clock gating checks part 1 | Static Timing Analysis tutorial | VLSI**

Lesson 32 : **sta lec31 clock gating checks part 2 | Static Timing Analysis tutorial | VLSI**

Lesson 33 : **sta lec32 clock gating checks part 3 | Static Timing Analysis tutorial | VLSI**

Lesson 34 : **sta lec33 introduction to OCV | Static Timing Analysis tutorial | VLSI**

Lesson 35 : **sta lec34 OCV concepts part2 | static timing analysis tutorial | VLSI**

Lesson 36 : **sta lec35 OCV concepts part3 | static timing analysis tutorial | VLSI**

Lesson 37 : **sta lec36 Interview Questions Part1 | static timing analysis tutorial | VLSI**

Lesson 38 : **sta lec37 interview question part2 | static timing analysis tutorial | VLSI**

Lesson 39 : **sta lec 38 Numerical Questions part3 | static timing analysis tutorial | VLSI**

Lesson 40 : **STA lec39 Latch Time Borrow | Static Timing Analysis tutorial | VLSI**

Lesson 41 : **Videos for Cats to Watch 8 Hour Birds Bonanza Cat TV Bird Watch**

Related courses

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