Mindluster Platform

About the course Computer architecture

Computer architecture course in hindi, In this course we'll explore the design and organization of computer systems, including CPUs, memory, and system components.

Computer Science Category's Courses

Course Lesson(129)

Lesson 1 : **Computer Architecture Organization by Engineering Funda**

Lesson 2 : **Introduction to Computer Organization Architecture**

Lesson 3 : **Computer Architecture in COA**

Lesson 4 : **Von Neumann Architecture in Computer Organization Architecture**

Lesson 5 : **Harvard Architecture in Computer Organization Architecture**

Lesson 6 : **Von Neumann Architecture Vs Harvard Architecture**

Lesson 7 : **Flynn s Classification in Computer Organization Architecture**

Lesson 8 : **RISC and CISC Architectures in Computer**

Lesson 9 : **Little Endian and Big Endian Computers**

Lesson 10 : **Examples of Little Endian and Big Endian Computer**

Lesson 11 : **GATE Questions of Computer Organization**

Lesson 12 : **1 Memory Interfacing Questions of Computer Organization Architecture**

Lesson 13 : **2 Memory Interfacing Questions of Computer Organization Architecture**

Lesson 14 : **3 Memory Interfacing Questions of Computer Organization Architecture**

Lesson 15 : **4 Memory Interfacing Questions of Computer Organization Architecture**

Lesson 16 : **Registers of Basic Computer in COA**

Lesson 17 : **Registers Organization in COA**

Lesson 18 : **Common Bus Access using MUX in Computer**

Lesson 19 : **Working of Computer using Common Bus in COA**

Lesson 20 : **Questions on Registers of Computer**

Lesson 21 : **Instruction Formats and ALU in Computer Organization Architecture**

Lesson 22 : **Examples on Instruction Formats in Computer Organization Architecture**

Lesson 23 : **Instruction Cycle in Computer Organization Architecture**

Lesson 24 : **Timings and Flowchart of Instruction Cycle in Computer Organization Architecture**

Lesson 25 : **Instruction Cycle State Transition Diagram in Computer Organization Architecture**

Lesson 26 : **Types of Instructions in Computer Organization Architecture**

Lesson 27 : **Addressing Modes in Computer Organization Architecture**

Lesson 28 : **1 Example of Addressing Modes in Computer Organization Architecture**

Lesson 29 : **2 Examples of Addressing Modes in Computer Organization Architecture**

Lesson 30 : **3 Examples of Addressing Modes in Computer Organization Architecture**

Lesson 31 : **4 Examples of Addressing Modes in Computer Organization Architecture**

Lesson 32 : **1 Examples on Instruction Execution in Computer Organization Architecture**

Lesson 33 : **2 Examples on Instruction Execution in Computer Organization Architecture**

Lesson 34 : **3 Examples on Instruction Execution in Computer Organization Architecture**

Lesson 35 : **1 Examples on Instruction Format in Computer Organization Architecture**

Lesson 36 : **2 Examples on Instruction Format in Computer Organization Architecture**

Lesson 37 : **3 Examples on Instruction Format in Computer Organization Architecture**

Lesson 38 : **4 Examples on Instruction Format in Computer Organization Architecture**

Lesson 39 : **CPU Execution Time Average CPI of CPU MIPS | CPU Performance Parameters**

Lesson 40 : **Examples of CPU Performance in COA**

Lesson 41 : **CPU Performance Parameters in Computer Organization Architecture**

Lesson 42 : **Amdahl s Law in Computer Organization Architecture**

Lesson 43 : **1 Examples of CPU Performance in Computer Organization Architecture**

Lesson 44 : **2 Examples of CPU Performance in Computer Organization Architecture**

Lesson 45 : **Micro Operations of Instruction in Computer Organization Architecture**

Lesson 46 : **Hardwired Control Unit in Computer Organization Architecture**

Lesson 47 : **Wilkes Design for Microprogrammed Control Unit**

Lesson 48 : **Microprogrammed Control Unit in Computer Organization Architecture**

Lesson 49 : **Hardwired Control Unit Vs Microprogrammed Control Unit**

Lesson 50 : **Horizontal Microinstruction Vs Vertical Microinstruction**

Lesson 51 : **Nano Programming in Computer Organization Architecture**

Lesson 52 : **Microinstruction Format in Computer Organization Architecture**

Lesson 53 : **1 Examples based on Microinstruction in Computer Organization Architecture**

Lesson 54 : **2 Examples based on Microinstruction in Computer Organization Architecture**

Lesson 55 : **3 Examples based on Microinstruction in Computer Organization Architecture**

Lesson 56 : **Examples of Control Unit in Computer Organization Architecture**

Lesson 57 : **Registers Organization in RISC Processor in Computer Organization Architecture**

Lesson 58 : Pipelining in Computer Organization Architecture

Lesson 59 : Parameters of Pipelining in Computer Organization Architecture

Lesson 60 : Structural Hazards in Pipelining

Lesson 61 : Data Hazards in Pipelining

Lesson 62 : Control Hazards in Pipelining

Lesson 63 : 1 Examples on Pipelining in Computer Organization Architecture

Lesson 64 : 2 Examples on Pipelining in Computer Organization Architecture

Lesson 65 : 3 Examples on Pipelining in Computer Organization Architecture

Lesson 66 : 4 Examples on Pipelining in Computer Organization Architecture

Lesson 67 : 5 Examples on Pipelining in Computer Organization Architecture

Lesson 68 : 6 Examples on Pipelining Hazards in Computer Organization Architecture

Lesson 69 : 1 Non Synchronized Pipeline in Computer Organization Architecture

Lesson 70 : 2 Non Synchronized Pipeline in Computer Organization Architecture

Lesson 71 : CPI and Speed Up in Pipelining with Hazards

Lesson 72 : Examples on Pipelining Hazards in Computer Organization Architecture

Lesson 73 : Why Computer prefers 2 s Complement in signed Data Representation

Lesson 74 : Signed Data Representation and Range of signed Numbers in Computer

Lesson 75 : 1 Examples on Number Representation

Lesson 76 : 2 Examples on Number Representation

Lesson 77 : Normalization of Floating Point Number

Lesson 78 : IEEE 754 Single Precision Floating Point Number Representation

Lesson 79 : IEEE 754 Double Precision Floating Point Number Representation

Lesson 80 : Extreme Cases of Floating Point Representation in IEEE 754 Format

Lesson 81 : 1 Examples on Floating Point Numbers

Lesson 82 : 2 Examples on Floating Point Numbers

Lesson 83 : 3 Examples on Floating Point Numbers

Lesson 84 : Half Adder and Full Adder in Computer Architecture and Organization

Lesson 85 : Serial Adder or Ripple Carry Adder in Computer Architecture and Organization

Lesson 86 : Carry Look Ahead Adder CLA Parallel Adder

Lesson 87 : Subtractor in Computer Architecture and Organization

Lesson 88 : 4 bits Adder Subtractor

Lesson 89 : 1 Booth s Algorithm Example in Computer Organization Architecture

Lesson 90 : 2 Booth s Algorithm Example in Computer Organization Architecture

Lesson 91 : Booth s Algorithm

Lesson 92 : **Restoring Division Algorithm**

Lesson 93 : **Non Restoring Division Algorithm**

Lesson 94 : **System Buses in Computer Organization and Architecture**

Lesson 95 : **Daisy Chain Polling Independent Request bus contention methods**

Lesson 96 : **Memory Organization in Computer**

Lesson 97 : **Locality of Reference Spatial Locality and Temporal Locality**

Lesson 98 : **Average Access Time of Memory and Access Speed of Memory**

Lesson 99 : **Example on Average Access Time of Memory and Access Speed of Memory**

Lesson 100 : **Cache Memory Organization**

Lesson 101 : **Example on Cache Memory Organization**

Lesson 102 : **Direct Address Mapping in Cache Memory**

Lesson 103 : **Example of Direct Address Mapping in Cache Memory**

Lesson 104 : **Fully Associative Address Mapping in Cache Memory**

Lesson 105 : **Example of Fully Associative Address Mapping in Cache Memory**

Lesson 106 : **Set Associative Address Mapping in Cache Memory**

Lesson 107 : **Example on Set Associative Address Mapping in Cache Memory**

Lesson 108 : **Replacement Strategies in Cache Memory**

Lesson 109 : **Example on Replacement Strategies in Cache Memory**

Lesson 110 : **Updation Techniques in Cache Memory**

Lesson 111 : **Write Through Updation Technique in Cache Memory**

Lesson 112 : **Write Back Updation Technique in Cache Memory**

Lesson 113 : **Multi Level Cache Memory**

Lesson 114 : **Types of Misses in Cache Memory**

Lesson 115 : **Examples on Mapping of Cache Memory**

Lesson 116 : **Examples on Mapping of Cache Memory**

Lesson 117 : **Examples on Multi Level Cache Memory**

Lesson 118 : **Example on Conflict Misses in Cache Memory**

Lesson 119 : **1 Examples on Cache Memory**

Lesson 120 : **2 Examples on Cache Memory**

Lesson 121 : **3 Examples on Cache Memory**

Lesson 122 : **Input Output Organization**

Lesson 123 : **Programmed IO**

Lesson 124 : **Interrupt Driven IO**

Lesson 125 : **Direct Memory Access DMA**

Lesson 126 : **Examples on Direct Memory Access**

Lesson 127 : **2 Examples on Direct Memory Access**

Lesson 128 : **Memory Mapped IO and IO Mapped IO in Microprocessor 8086**

Lesson 129 : **Computer Architecture Organization on Engineering Funda Android Application**

Related courses

Linear Algebra for Computer Scientists

Insertion Sort

Bubble Sort

GCSE Computer Science

Random Access Memory

Binary Trees

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