Mindluster Platform

About the course Digital Logic Families in Digital Electronics

Digital Logic Families course in hindi, in this course Explore various logic families, including TTL, CMOS, and ECL, and their applications in digital circuit design and semiconductor technology.

Engineering Category's Courses

Course Lesson(32)

Lesson 1 : **Digital Logic Families Classifications of Digital Logic Families in Digital Electronics**

Lesson 2 : **Merits Demerits and Characteristics of Digital IC DigitalIC DigitalElectronics**

Lesson 3 :
Propagation Delay Threshold Voltage Power Dissipation Figure of Merits DigitalElectronics

Lesson 4 :
Fan out Fan in and Operating Temperature of IC Characteristics of Digital IC DigitalElectronics

Lesson 5 :
Voltage and Current Parameters Noise Margin Characteristics of Digital IC DigitalElectronics

Lesson 6 :
Resistor Transistor Logic RTL RTL NOT Gate RTL NOR Gate Disadvantages of RTL DigitalElectronics

Lesson 7 :
Diode Transistor Logic DTL DTL NAND Gate Components Details of DTL Disadvantages of DTL DTL

Lesson 8 :
Transistor Transistor Logic TTL TTL NAND Circuit TTL NAND Gate Working TTL Components Details

Lesson 9 :
TTL NAND Gate with Totem Pole Output Circuit Working of TTL NAND Gate with Totem Pole Output

Lesson 10 :
TTL NOR Gate with Totem Pole Output Circuit Working of TTL NOR Gate with Totem Pole Output TTL

Lesson 11 :
TTL Open Collector NAND Gate Circuit Working of TTL Open Collector NAND Gate Digital Electronics

Lesson 12 :
TTL Tristate Logic Circuit Working of TTL Tristate Logic Logic Families Digital Electronics

Lesson 13 :
CMOS logic circuit rules Structure of CMOS Circuit CMOS Circuit in VLSI Digital Electronics

Lesson 14 :

CMOS NAND Gate Circuit of CMOS NAND Gate Working of CMOS NAND Gate Truth Table of CMOS NAND Gate

Lesson 15 :
CMOS NOR Gate Circuit of CMOS NOR Gate Working of CMOS NOR Gate Truth Table of CMOS NOR Gate

Lesson 16 :
Boolean Function Implementation using CMOS CMOS Circuit in VLSI Digital Electronics CMOSCircuit

Lesson 17 :
CMOS Multiplexer CMOS Multiplexer Circuit CMOS Multiplexer Working CMOS Multiplexer Truth Table

Lesson 18 :
CMOS Half Adder CMOS Half Adder Circuit CMOS Half Adder Working CMOS Half Adder Truth Table

Lesson 19 :
Boolean Function implementation using CMOS and equivalent size of CMOS Circuit CMOSCircuit

Lesson 20 :
CMOS SR Latch using NOR Gates CMOS SR Latch using NOR Gates Circuit Working Truth Table

Lesson 21 :
CMOS SR Latch using NAND Gates CMOS SR Latch using NAND Gates Circuit Working Truth Table

Lesson 22 :
CMOS D Latch CMOS D Latch Circuit CMOS D Latch Working CMOS D Latch Truth Table CMOSCircuit

Lesson 23 :
CMOS D Latch CMOS D Latch Circuit CMOS D Latch Working CMOS D Latch Truth Table CMOSCircuit

Lesson 24 :
CMOS SR Flip Flop using NOR Gates CMOS SR Flip Flop using NOR Gates implementation CMOSCircuit

Lesson 25 :
CMOS JK Flip Flop using NOR Gates CMOS JK Flip Flop using NOR Gates implementation CMOSCircuit

Lesson 26 :
Stick Diagram of CMOS NOR Gate CMOS NOR Gate Circuit in VLSI and Digital Electronics CMOSCircuit

Lesson 27 :
Stick Diagram of CMOS NAND Gate CMOS NAND Gate Circuit in VLSI and Digital Electronics

Lesson 28 :
Stick Diagram of Boolean Function CMOS Boolean Function Circuit in VLSI and Digital Electronics

Lesson 29 :
CMOS Transmission Gate Working of CMOS Transmission Gate CMOS Transmission Gate as Tri State Buffe

Lesson 30 :
Multiplexer Implementation using Transmission Gates 4 to 1 Multiplexer in VLSI Digital Electronic

Lesson 31 :
D Latch Implementation using Transmission Gate CMOS Transmission Gate in VLSI Digital Electronics

Lesson 32 :

Boolean Function Implementation using Transmission Gates Transmission Gates Examples CMOSCircuit

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