Mindluster Platform

About the course CMOS Logic Circuit Basics

CMOS Logic Circuit course in hindi, in this course Explore the fundamentals and design principles of Complementary Metal-Oxide-Semiconductor logic circuits for digital electronics.

Engineering Category's Courses

Course Lesson(29)

Lesson 1 :
CMOS logic circuit rules Structure of CMOS Circuit CMOS Circuit in VLSI Digital Electronics

Lesson 2 :
CMOS NAND Gate Circuit of CMOS NAND Gate Working of CMOS NAND Gate Truth Table of CMOS NAND Gate

Lesson 3 :
CMOS NOR Gate Circuit of CMOS NOR Gate Working of CMOS NOR Gate Truth Table of CMOS NOR Gate

Lesson 4 :
Boolean Function Implementation using CMOS CMOS Circuit in VLSI Digital Electronics CMOSCircuit

Lesson 5 :
CMOS Multiplexer CMOS Multiplexer Circuit CMOS Multiplexer Working CMOS Multiplexer Truth Table

Lesson 6 :
CMOS Half Adder CMOS Half Adder Circuit CMOS Half Adder Working CMOS Half Adder Truth Table

Lesson 7 :
Boolean Function implementation using CMOS and equivalent size of CMOS Circuit CMOSCircuit

Lesson 8 :
CMOS SR Latch using NOR Gates CMOS SR Latch using NOR Gates Circuit Working Truth Table

Lesson 9 :
CMOS SR Latch using NAND Gates CMOS SR Latch using NAND Gates Circuit Working Truth Table

Lesson 10 :
CMOS D Latch CMOS D Latch Circuit CMOS D Latch Working CMOS D Latch Truth Table CMOSCircuit

Lesson 11 :
CMOS SR Flip Flop using NOR Gates CMOS SR Flip Flop using NOR Gates implementation CMOSCircuit

Lesson 12 :
CMOS JK Flip Flop using NOR Gates CMOS JK Flip Flop using NOR Gates implementation CMOSCircuit

Lesson 13 :

Stick Diagram of CMOS NOR Gate CMOS NOR Gate Circuit in VLSI and Digital Electronics CMOSCircuit

Lesson 14 :
Stick Diagram of CMOS NAND Gate CMOS NAND Gate Circuit in VLSI and Digital Electronics

Lesson 15 :
Stick Diagram of Boolean Function CMOS Boolean Function Circuit in VLSI and Digital Electronics

Lesson 16 :
CMOS Transmission Gate Working of CMOS Transmission Gate CMOS Transmission Gate as Tri State Buffe

Lesson 17 :
Multiplexer Implementation using Transmission Gates 4 to 1 Multiplexer in VLSI Digital Electronic

Lesson 18 :
Multiplexer Implementation using Transmission Gates 4 to 1 Multiplexer in VLSI Digital Electronic

Lesson 19 :
D Latch Implementation using Transmission Gate CMOS Transmission Gate in VLSI Digital Electronics

Lesson 20 :
Boolean Function Implementation using Transmission Gates Transmission Gates Examples CMOSCircuit

Lesson 21 :
Boolean Function Implementation using Transmission Gates Transmission Gates Examples CMOSCircuit

Lesson 22 :
Dynamic CMOS Circuit Working of Dynamic CMOS Advantages Disadvantages of Dynamic CMOS

Lesson 23 :
Cascading Issues of Dynamic CMOS Solution of Cascading Issues in Dynamic CMOS Dynamic CMOS

Lesson 24 :
Comparison of Static and Dynamic CMOS Static CMOS Dynamic CMOS in VLSI by Engineering Funda

Lesson 25 :
Domino Logic CMOS Circuit of Domino Logic CMOS Working of Domino Logic CMOS by Engineering Funda

Lesson 26 :
Charge Sharing in Dynamic CMOS Solution of Charge Sharing in Dynamic CMOS Dynamic CMOS

Lesson 27 :
Boolean Function Implementation using Dynamic CMOS Structure of Dynamic CMOS Dynamic CMOS

Lesson 28 :
NORA CMOS Logic NP CMOS Logic Basics of NORA CMOS Logic Structure Working of NORA CMOS Logic

Lesson 29 :
Boolean Function Implementation using NORA CMOS Logic NORA CMOS Logic by Engineering Funda

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