Mindluster Platform

About the course Memory Interfacing Fundamentals

Memory Interfacing Fundamentals course, in this course In this course, we delve into essential concepts of interfacing memory devices in computer systems.

Computer Science Category's Courses

Course Lesson(45)

Lesson 1 : **Introduction to Memory**

Lesson 2 : **Memory Hierarchy Interfacing**

Lesson 3 : **Memory Interfacing â€“ Solved PYQs**

Lesson 4 : **Introduction to Cache Memory**

Lesson 5 : **Direct Memory Mapping**

Lesson 6 : **Direct Memory Mapping â€“ Solved Examples**

Lesson 7 : **Direct Memory Mapping â€“ Solved PYQs Part 1**

Lesson 8 : **Direct Memory Mapping â€“ Solved PYQs Part 2**

Lesson 9 : **Direct Memory Mapping â€“ Solved PYQs Part 3**

Lesson 10 : **Direct Memory Mapping â€“ Hardware Implementation**

Lesson 11 : **Associative Mapping**

Lesson 12 : **Associative Mapping â€“ Solved Examples**

Lesson 13 : **Associative Mapping â€“ Solved PYQ**

Lesson 14 : **Set Associative Mapping**

Lesson 15 : **Set Associative Mapping â€“ Solved Examples Part 1**

Lesson 16 : **Set Associative Mapping â€“ Solved Examples Part 2**

Lesson 17 : **Set Associative Mapping â€“ Solved PYQs Part 1**

Lesson 18 : **Set Associative Mapping â€“ Solved PYQs Part 2**

Lesson 19 : **Cache Memory Mapping â€“ A Comparative Study**

Lesson 20 : **Cache Memory Mapping â€“ Solved PYQ**

Lesson 21 : **Set Associative Mapping â€“ Bonus PYQs**

Lesson 22 : **Cache Design An Overview**

Lesson 23 : **Cache Replacement Policies RR FIFO LIFO Optimal**

Lesson 24 : **Cache Replacement Policies MRU LRU Pseudo LRU LFU**

Lesson 25 : **LRU Cache Replacement Policy Solved PYQs**

Lesson 26 : **Cache Coherence Problem Cache Coherency Protocols**

Lesson 27 : **Snooping based Cache Coherency Protocol**

Lesson 28 : **Directory based Cache Coherency Protocol**

Lesson 29 : **Introduction to Primary Memory**

Lesson 30 : **Primary Memory â€“ Architecture of ROM Part 1**

Lesson 31 : **Primary Memory â€“ Architecture of ROM Part 2**

Lesson 32 : **Primary Memory â€“ Architecture of ROM Part 3**

Lesson 33 : **Primary Memory â€“ Architecture of ROM Part 4**

Lesson 34 : **ROM â€“ Solved Examples**

Lesson 35 : **Primary Memory â€“ RAM**

Lesson 36 : **Primary Memory â€“ Solved Example**

Lesson 37 : **Introduction to Secondary Memory**

Lesson 38 : **Secondary Memory â€“ Hard Disk Drives**

Lesson 39 : **Hard Disk Drives Solved Problems Set 1**

Lesson 40 : **Hard Disk Drives â€“ Recording Density Rotational Speed**

Lesson 41 : **Hard Disk Drives Solved Problems Set 2**

Lesson 42 : **Hard Disk Drives Solved Problems Set 3**

Lesson 43 : **Hard Disk Drives Solved Problems Set 4**

Lesson 44 : **Hard Disk Drives Solved Problems Set 5**

Lesson 45 : **Secondary Memory â€“ Solid State Drives**

Related courses

Linear Algebra for Computer Scientists

Insertion Sort

Bubble Sort

GCSE Computer Science

Random Access Memory

Binary Trees

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